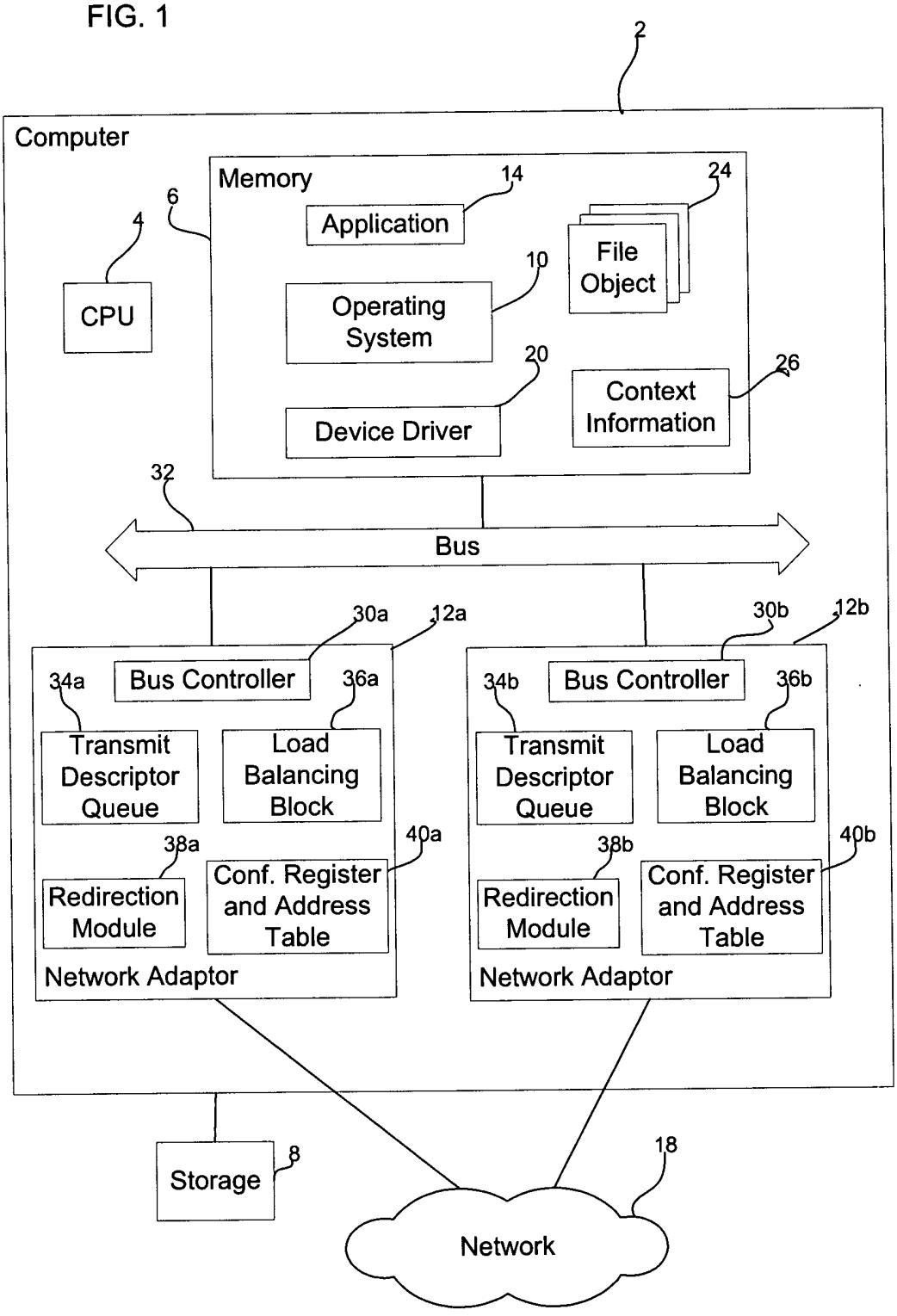
FIG. 1


2
Computer
Memory
14
24
6
4
Application
File Object
10
CPU
Operating System
20
26
Context Information
Device Driver
32
Bus
30a
12a
30b
12b
34a
Bus Controller
36a
34b
Bus Controller
36b
Transmit Descriptor Queue
Load Balancing Block
Transmit Descriptor Queue
Load Balancing Block
40a
38a
38b
40b
Redirection Module
Conf. Register and Address Table
Redirection Module
Conf. Register and Address Table
Network Adaptor
Network Adaptor
Storage
8
18
Network

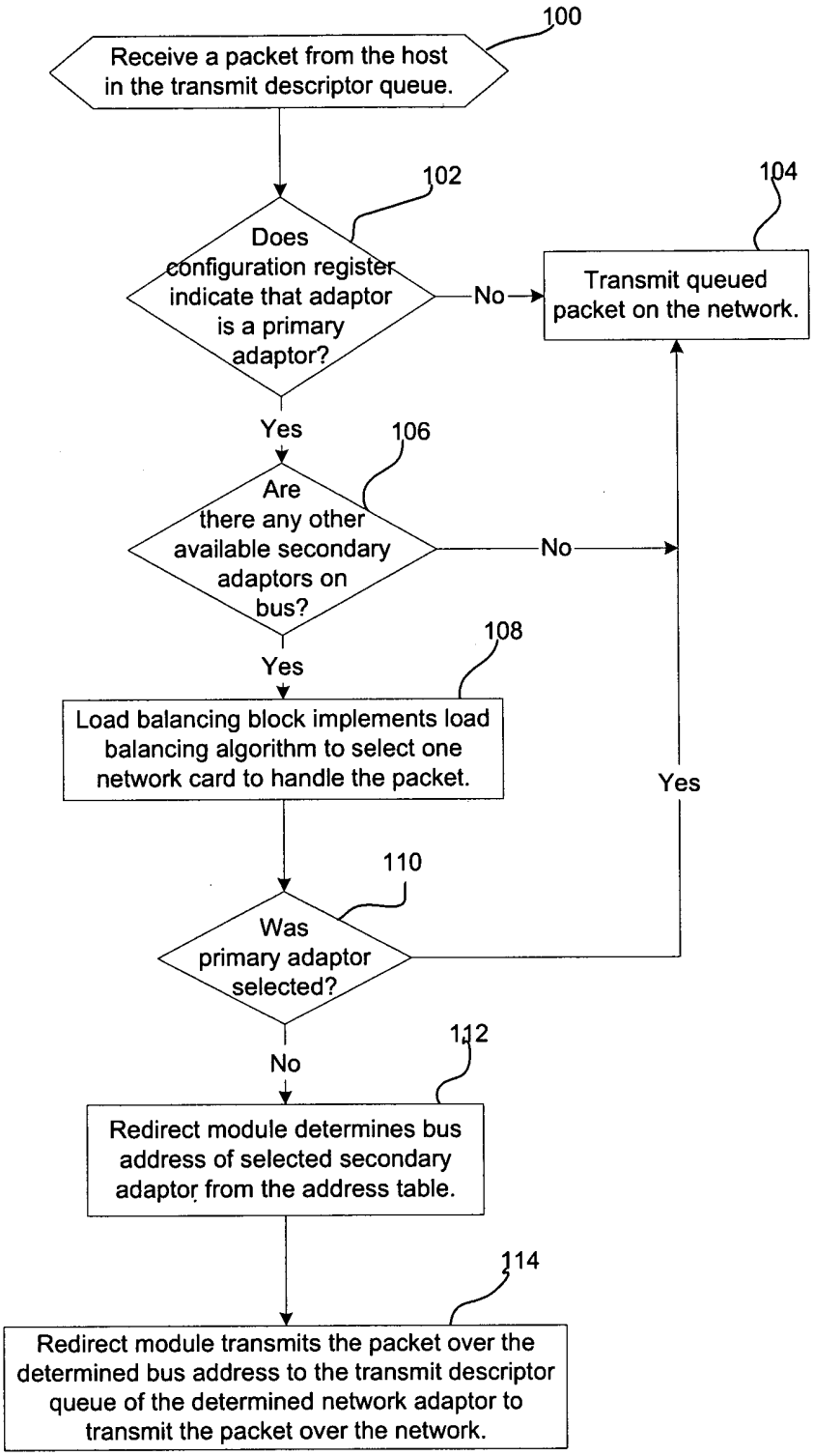
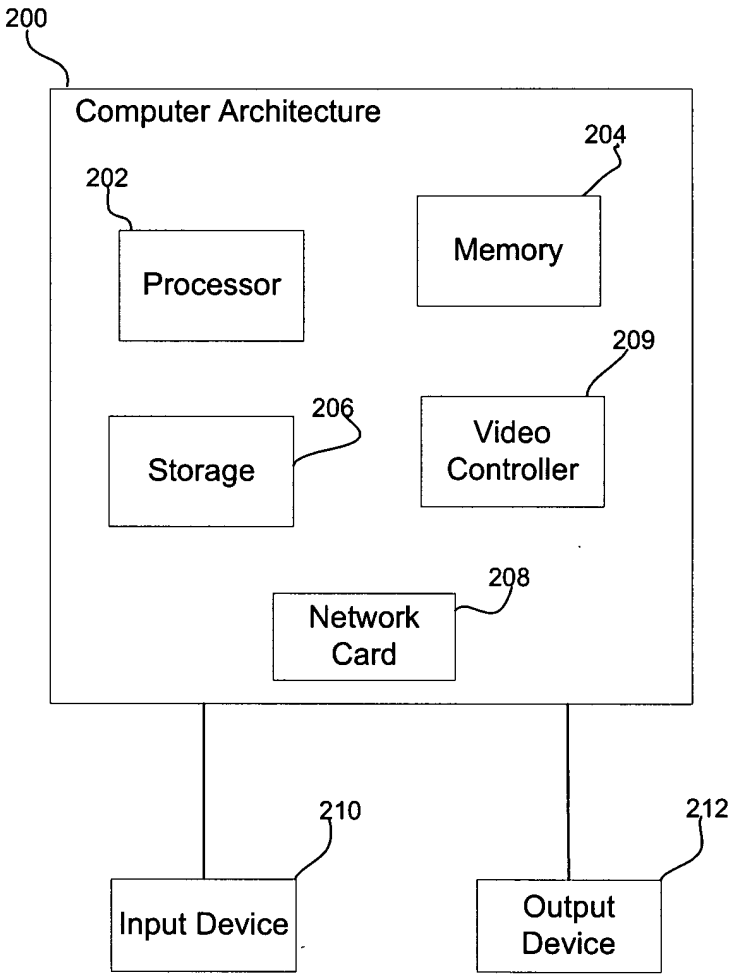

100
Receive a packet from the host in the transmit descriptor queue.
102
Does configuration register indicate that adaptor is a primary adaptor?
No
104
Transmit queued packet on the network.
Yes
106
Are there any other available secondary adaptors on bus?
No
Yes
108
Load balancing block implements load balancing algorithm to select one network card to handle the packet.
Yes
110
Was primary adaptor selected?
No
112
Redirect module determines bus address of selected secondary adaptor from the address table.
114
Redirect module transmits the packet over the determined bus address to the transmit descriptor queue of the determined network adaptor to transmit the packet over the network.

FIG. 2

FIG. 3


200
Computer Architecture
202
Processor
204
Memory
209
Video Controller
206
Storage
208
Network Card
210
Input Device
212
Output Device